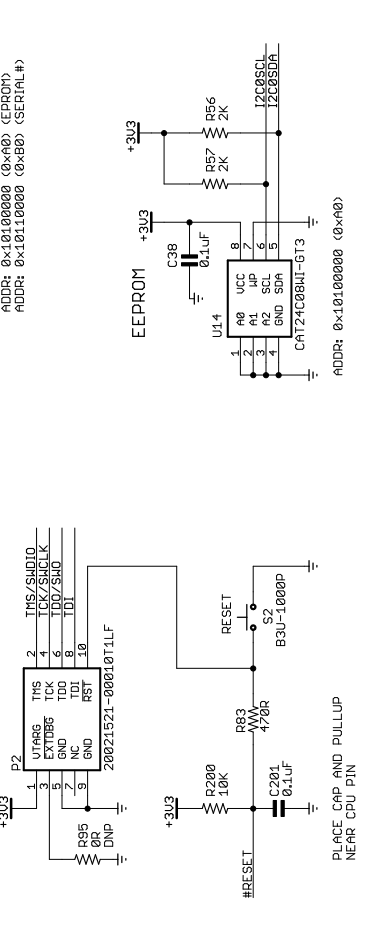
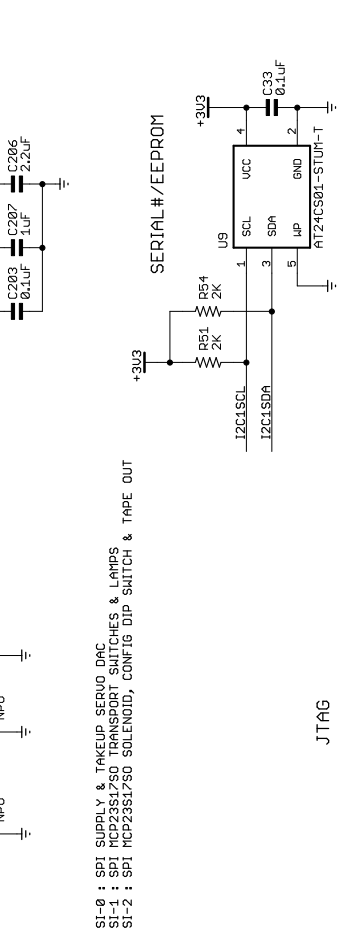
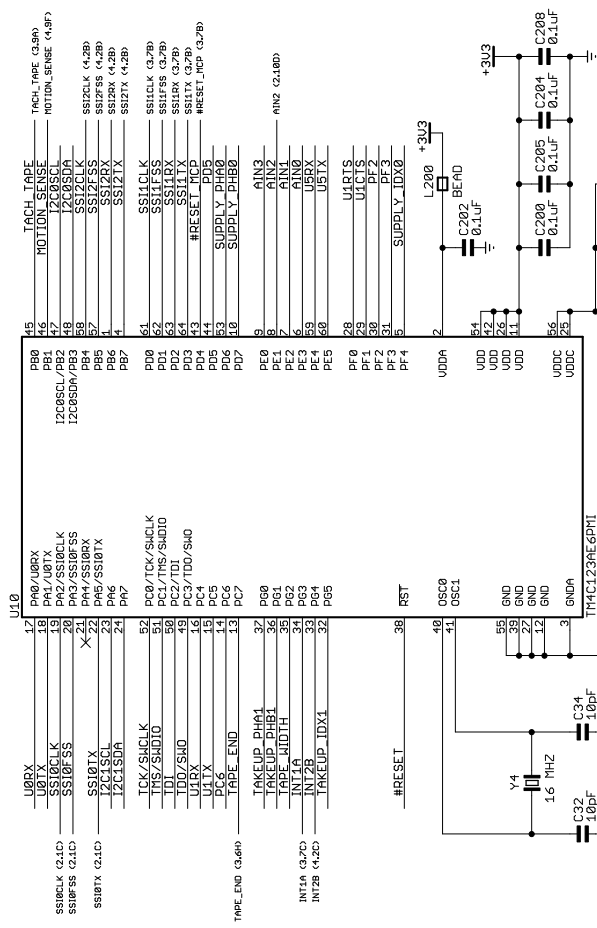


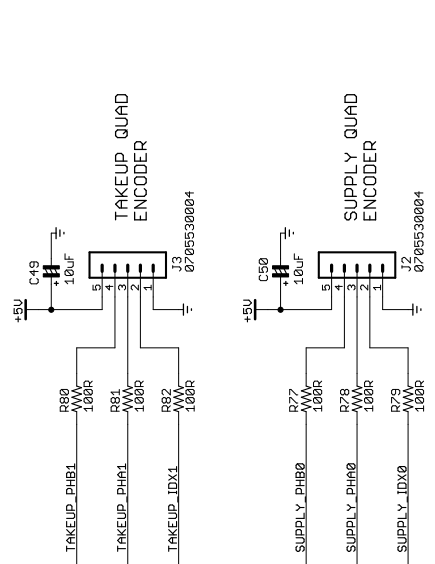
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PROPRIETARY

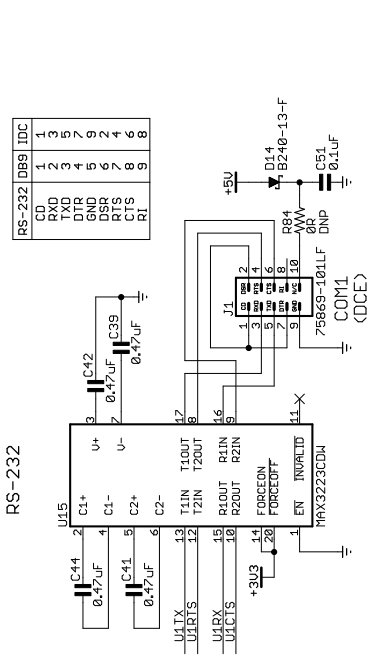
MICROCONTROLLER



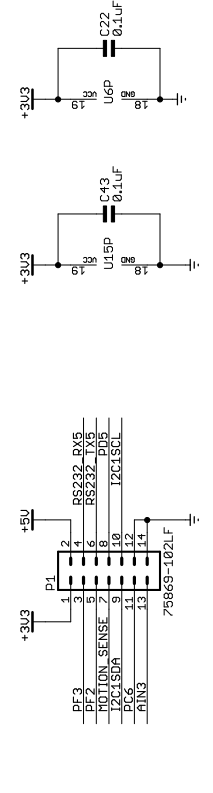
QUADRATURE ENCODER INTERFACES



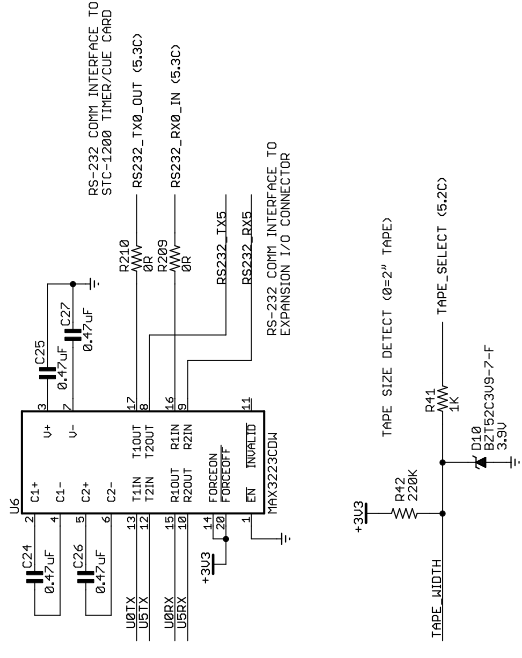
RS-232



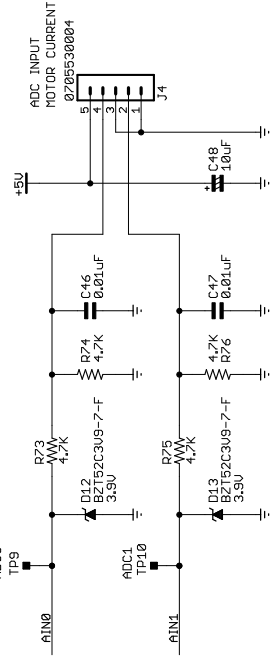
EXPANSION



RS-232 INTER-BOARD COMM



OPTION ADC INPUTS



CONN HOUSING  
 BOM-ENTRY1  
 50-57-3405  
 QTY = 3  
 BOM-ENTRY2  
 16-02-0057  
 QTY = 15  
 IDC CONNECTOR  
 BOM-ENTRY3  
 71600-110LF  
 QTY = 2  
 IDC CONNECTOR  
 BOM-ENTRY4  
 8309-6000  
 QTY = 2

ISSUE	DATE	BY	REV
DRAN	XX/XX/XX		C
CHECKED	DATE	DRG NO	
R-STARR	4/8/2017	ED-2XXX.100	
R-STARR	6:44 PM		
DATE	FILE: DTC-1200-C		
XX/XX/XX			

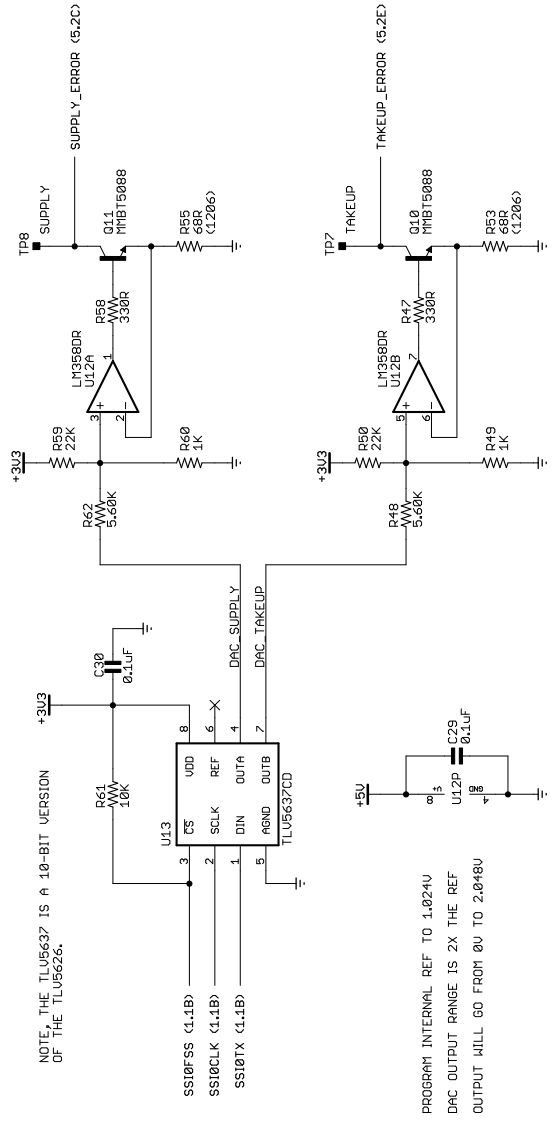
PLACE CAP AND PULLUP NEAR CPU PIN

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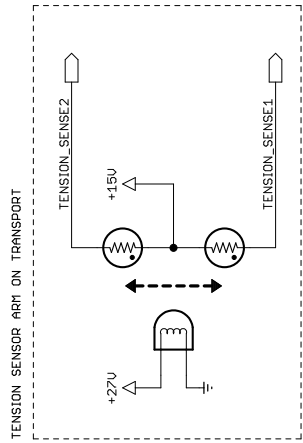
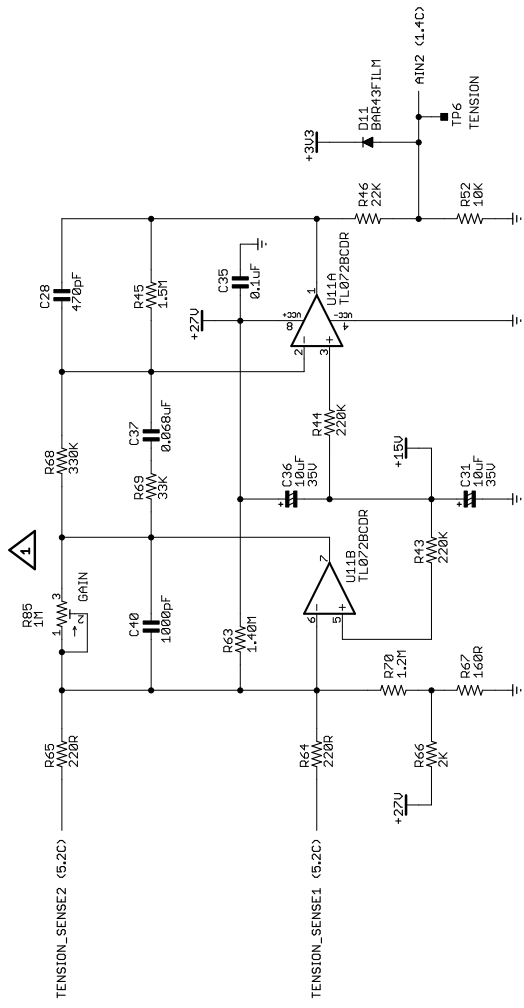
**MOTOR DRIVE AMP DRIVERS**  
 5.5 mA = NO TORQUE  
 1.1 mA = FULL TORQUE

**MOTOR DRIVE AMP CURRENT DAC**



NOTE, THE TLU5637 IS A 10-BIT VERSION OF THE TLU5626.  
 PROGRAM INTERNAL REF TO 1.024V  
 DAC OUTPUT RANGE IS 2X THE REF  
 OUTPUT WILL GO FROM 0V TO 2.048V

**TENSION SENSOR PHOTOCELL AMP**

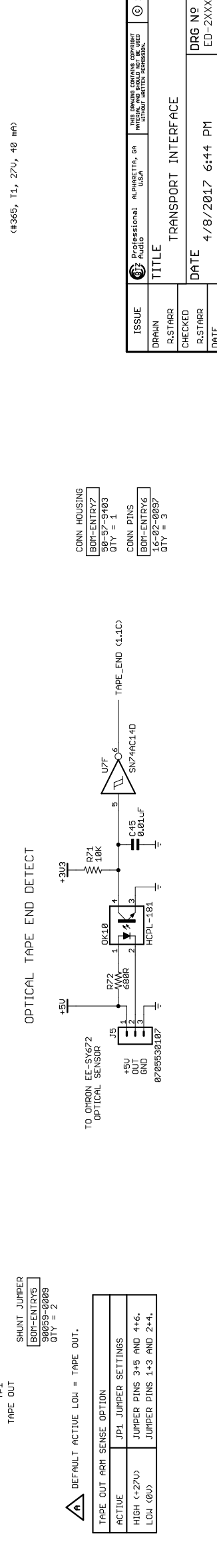
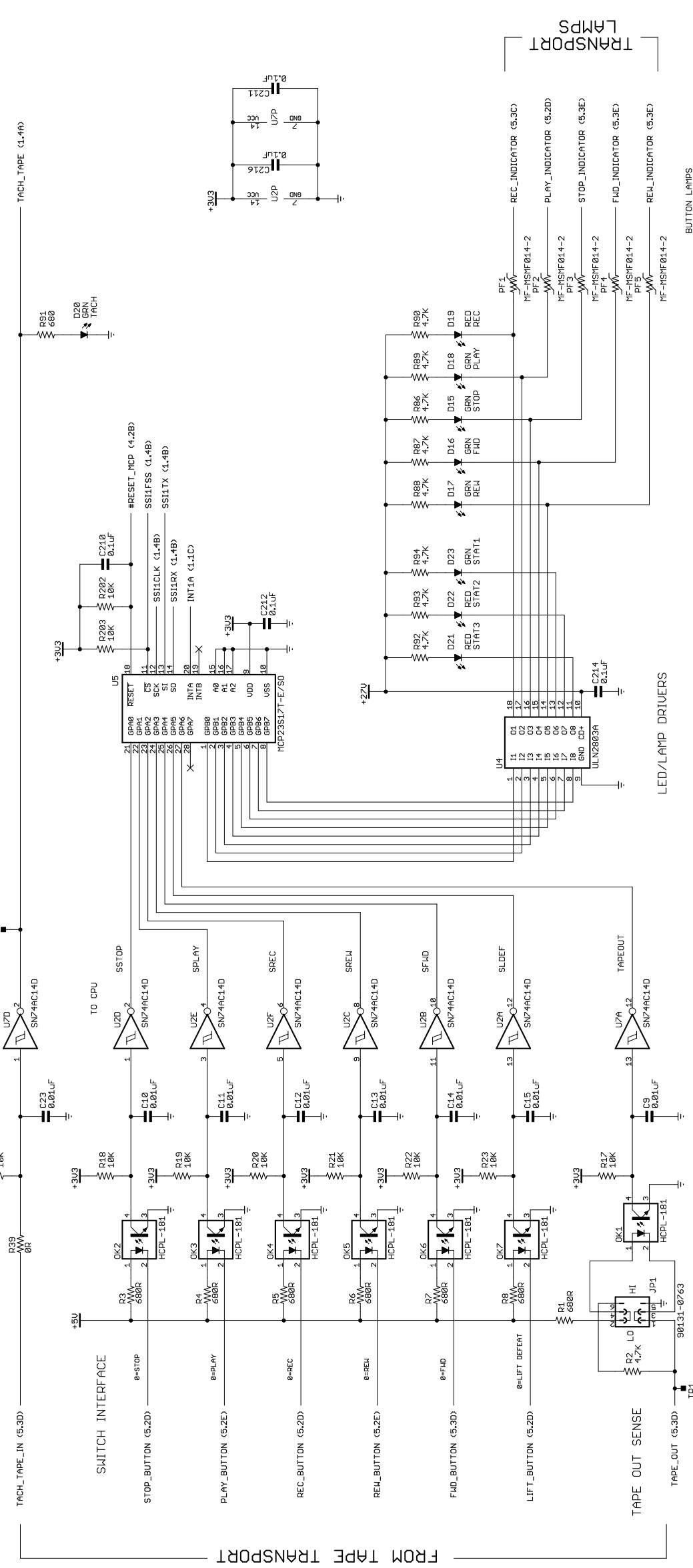


ADJUST TRIMMER R85 FOR MID SCALE  
 DAC READING OF XXXX WITH NO PRESSURE  
 ON THE TENSION SENSOR ARM.

When the DAC outputs 2.05V (the max value for code 255), U12A-3 should be 0.387U.  
 When the DAC outputs 0V (the value for code 0), U12A-3 should be 0.123U.  
 The math problem is 2 equations in 2 unknowns. The method I used is plain-old DC circuit analysis, sum of currents at U12A-3 = 0:  
 #1  
 Sum of currents through Rpu and R53 into node at U12A-2 = Current out of node U12A-2 through R55 to 0V.  
 $(3.3U - 0.387U) / Rpu + (2.05U - 0.387U) / R53 = 0.387U / 1K$   
 #2  
 Current through Rpu into node at U12A-2 = sum of currents out of node U12A-2 through R53 and R31 to 0V.  
 $(3.3U - 0.123U) / Rpu = 0.123U / R53 + 0.123U / 1K$   
 Rearrange both equations to isolate Rpu  
 #1b  
 $Rpu = 3.477 / [ (0.000123 + (0.123 / R53) ]$   
 #2b  
 $Rpu = 2.813 / [ (0.000387 - (1.663 / R53) ]$   
 Now, the right sides of the 2 equations are equal to each other, since they're both = Rpu, solve for R53 and you get 5.65K.  
 Then, plug 5.65K into 1b and Rpu = 21.947K.  
 Since we use standard values 22K & 5.6K, the DAC range 0 - 255 will give you a little more than 0.123U to 0.387U, but it will be very close.  
 This will result in a DAC range of about 5 - 250, or about 0.4% steps.

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CHECKED	R-STARR	TITLE MDA DAC / TENSION
R-DATE	4/8/2017 6:44 PM	DRG NO ED-2XXX.100
XX/XX/XX	FILE: DTC-1200-C	PAGE: 2/6

FROM TAPE TRANSPORT



**A** DEFAULT ACTIVE LOW = TAPE OUT.

TAPE OUT ARM SENSE OPTION	JPL JUMPER SETTINGS
ACTIVE	JUMPER PINS 3+5 AND 4+6.
HIGH (+27V)	JUMPER PINS 1+3 AND 2+4.
LOW (0V)	JUMPER PINS 1+3 AND 2+4.

SHUNT JUMPER  
 80M-ENTRY75  
 30059-0009  
 QTY = 2

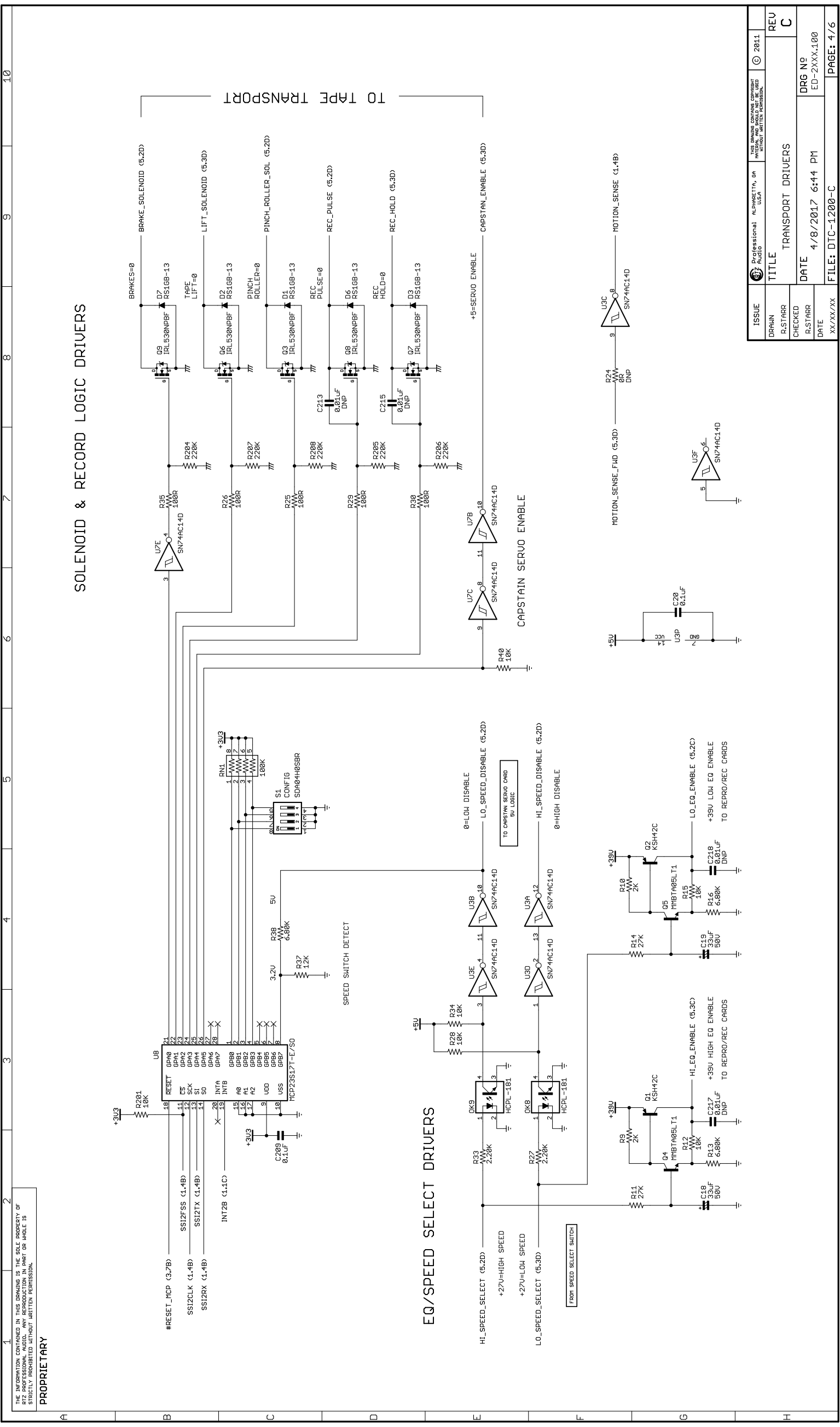
TO ORION EE-SY672  
 OPTICAL SENSOR  
 +5V  
 OUT  
 GND  
 HCPL-181  
 R7 680R  
 R21 10K  
 C45 0.01uF  
 U7F SN74AC14  
 TAPE\_END (L11C)

CONN HOUSING  
 80M-ENTRY7  
 50-57-9403  
 QTY = 1  
 CONN PINS  
 80M-ENTRY6  
 16-02-0097  
 QTY = 3

ISSUE  
 DRAM  
 R-STARR  
 CHECKED  
 R-STARR  
 DATE  
 XX/XX/XX

TITLE  
 TRANSPORT INTERFACE

DATE  
 4/8/2017 6:44 PM  
 DRG NO  
 ED-2XXX.100



# SOLENOID & RECORD LOGIC DRIVERS

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ISSUE DRAM CHECKED R-STARR DATE XX/XX/XX	TITLE TRANSPORT DRIVERS	DATE 4/8/2017 6:44 PM	DRG NO ED-2XXX.100	REV C
FILE: DTC-1200-C				PAGE: 4/6

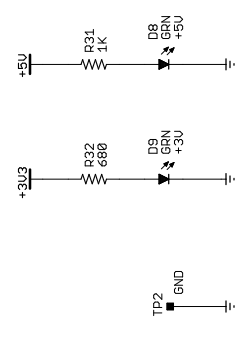
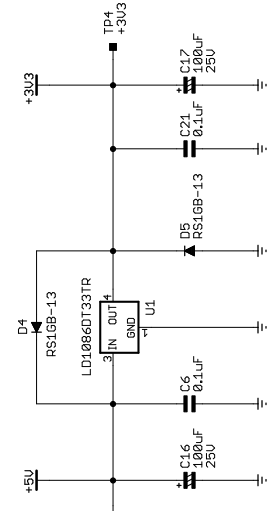
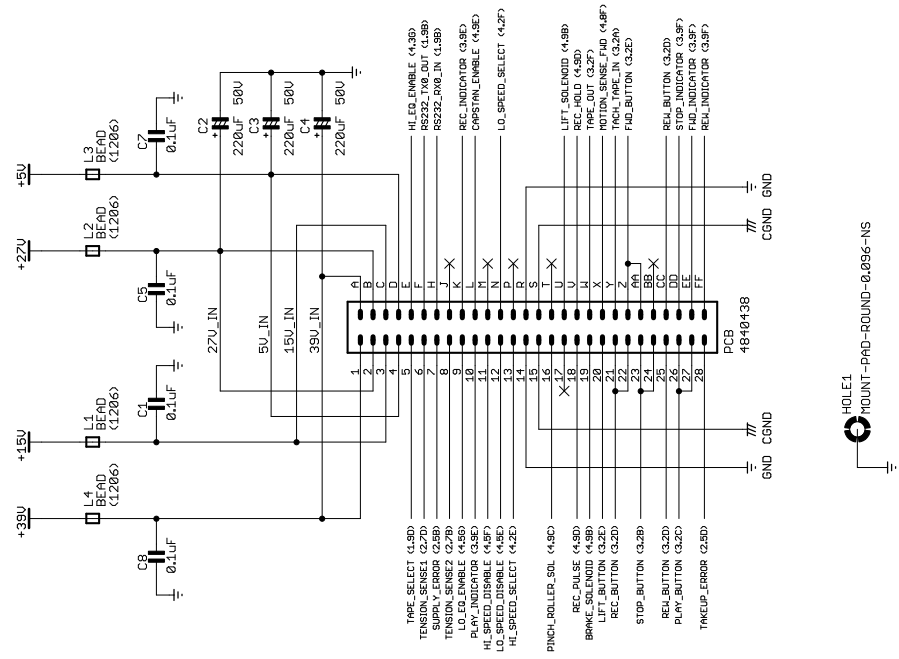
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**PROPRIETARY**

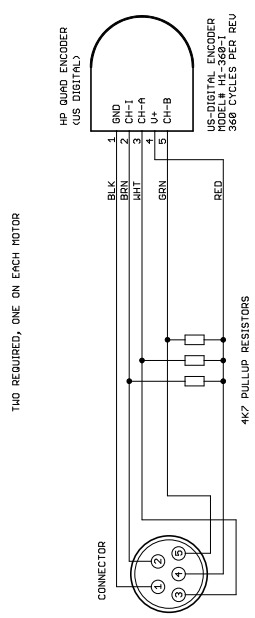
### PCB 4840438 EDGE CARD SIGNALS

PIN	DESCRIPTION	PIN	DESCRIPTION
1	+39V	A	+39V
2	+27V	B	+27V
3	+15V	C	+15V
4	+5V	D	+5V
5	TAPE SELECT (Ø=2")	E	HI EQ ENABLE (RS232 TX TO STC-1200)
6	TENSION SENSOR	F	(RS232 RX FROM STC-1200)
7	SUPPLY ERROR SIGNAL	H	---
8	TENSION SENSOR	J	---
9	LOW EQ ENABLE	K	REC INDICATOR
10	PLAY INDICATOR	L	CAPSTAN SERVO ENABLE
11	HI SPEED DISABLE	M	---
12	LO SPEED DISABLE	N	LO SPEED SELECT
13	HI SPEED SELECT	P	---
14	GND	R	GND
15	GND	S	GND
16	PINCH ROLLER SOLENOID	T	---
17	---	U	TAPE LIFTER SOLENOID
18	REC PULSE	V	REC HOLD
19	BRAKE SOLENOID	W	TAPE OUT SWITCH
20	LIFT DEFEAT BUTTON	X	MOTION SENSE (Ø=FWD)
21	REC BUTTON	Y	(TACH INPUT FROM STC/CTR)
22	REC BUTTON	Z	FWD BUTTON
23	STOP BUTTON	AA	FWD BUTTON
24	STOP BUTTON	BB	MOTION SENSE (Ø=REW)
25	REW BUTTON	CC	REW BUTTON
26	PLAY BUTTON	DD	STOP INDICATOR
27	PLAY BUTTON	EE	FWD INDICATOR
28	TAKEUP ERROR SIGNAL	FF	REW INDICATOR

NOTE: Unused pin Y on PM1200 chassis edge connector J1 must be wired to connector J4 pin 22 (tach out) from the timer/counter card to pickup tach pulses from the tape counter wheel.



### ENCODER ASSY WIRING DIAGRAM




### ENCODER/CONNECTOR WIRING

CONNECTOR PIN	WIRE COLOR	ENCODER DESIGNATION
1	BLACK	GND
2	BROWN	CH-I
3	WHITE	CH-A
4	RED	U+
5	GREEN	CH-B

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1	2	3	4	5	6	7	8	9	10
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<h2>REQUIRED WIRING MODIFICATIONS TO THE MM1200</h2>									
<p>1. THE SEARCH-TO-CUE/COUNTER CARD GENERATES TACH PULSES ON J4 PIN-22. THESE PULSES ARE DERIVED FROM THE TAPE ROLLER/COUNTER QUADRATURE ENCODER TO PROVIDE TAPE SPEED FEEDBACK. THESE TACH PULSES MUST BE ROUTED TO J1 PIN-Y ON THE NEW TRANSPORT CONTROLLER CARD FOR PROPER PLAY MODE OPERATION.</p>									
<p>2. THIS REQUIRES REMOVING THE CONTROL ELECTRONICS CARD CAGE FROM THE MM1200 AND ADDING A JUMPER WIRE FROM EDGE CONNECTOR J4 PIN-22 (STC/COUNTER CARD) AND RUNNING A WIRE OVER TO EDGE CONNECTOR J1 PIN-Y (TRANSPORT CTL CARD) ON THE CARD CAGE CONNECTORS.</p>									
<p>3. NOTE THAT THERE IS ALREADY A WIRE ON J4 PIN-22 WHICH FEEDS THE TACH OUTPUT TO J13 ACCESSORY CONNECTOR PIN AA. THIS WIRE SHOULD REMAIN AND THE JUMPER WIRE SHOULD CONNECT TO THIS SAME WIRE ON PIN-22 OF J4. THEN, ROUTE THE NEW WIRE FROM HERE TO J1 PIN-Y ON THE TRANSPORT CONTROL CARD SLOT. NOW THE TACH PULSES WILL BE AVAILABLE ON J1 PIN-Y FOR THE DTC-1200.</p>									
<p>4. THE 5V POWER FOR THE TRANSPORT CONTROL CARD SLOT COMES FROM THE CAPSTAN SERVO CARD IN THE ORIGINAL AMPLEX DESIGN. THE ZENER REGULATOR ON THE CAPSTAN DOES NOT HAVE ENOUGH CURRENT CAPABILITY TO POWER THE NEW DTC-1200 DIGITAL TRANSPORT CONTROLLER CARD.</p>									
<p>5. THEREFORE, J1 WIRING MUST BE CHANGED TO DRAW 5V POWER FROM THE MAIN SYSTEM 5V REGULATOR INSTEAD. THE MAIN SYSTEM +5V RAIL IS AVAILABLE AT J4 (SEARCH-TO-CUE) PINS I/FF &amp; A/28. THE NEW CONTROLLER CARD DRAWS ABOUT 220MA TOTAL CURRENT IN NORMAL OPERATION AND MUST NOW RUN FROM THE SYSTEM +5V RAIL.</p>									
<p>6. DISCONNECT THE +5V OUTPUT WIRE FROM J2 (CAPSTAN SERVO CARD) PINS 4 &amp; D AND MOVE THIS TO PINS FF &amp; 28 ON J4 (SEARCH-TO-CUE CARD) INSTEAD. THIS WILL POWER THE NEW TRANSPORT CONTROLLER FROM THE MAIN SYSTEM 5V SUPPLY WITH 2A TOTAL MAX CURRENT.</p>									
<p>7. JUMPERS WIRES ARE REQUIRED FOR RS-232 COMMUNICATIONS BETWEEN THE STC-1200 CUE/TIMER CARD AND THE DTC-1200 TRANSPORT CONTROL CARD ON EDGE CONNECTOR PINS F &amp; H OF BOTH CARDS.</p>									
<p>8. JUMPER A WIRE BETWEEN J1 PIN-F (TRANSPORT CTRL CARD) TO J4 PIN-F (STC/COUNTER CARD). ADDITIONALLY, JUMPER A WIRE BETWEEN J1 PIN-H TO J4 PIN-H.</p>									

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